

NEO[®] 94V-0



RXD1
UART

TXD1

WLED
LED

LINK0

IO42

IO41

IO40

IO39

RST
KEY

WPS

GND
PWR

3V3

MCLK

SDI

SDO
I2S

LRCK

BCLK

SCL
I2C

SDA



Open Source
hardware

BOARD: Widora-NEO-SEER
DESIGNED BY MANGO

<https://widora.io>

IO18
IO19
IO20
IO21

3 2

RXD2 TXD2
UART
PWM
GPIO

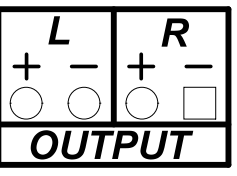
SPI

IO14
IO15
IO16
IO17
IO100

GND CK CS0 MO MI CS1 VF

0 1

D2 D3 CMD CLK D0 D1 CD WP



J1

5V GND GND 3V3

CNT OFF